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 **PALM INTRANET**

## Inventor Information for 10/813143

<b>Inventor Name</b>	<b>City</b>	<b>State/Country</b>
TATE, LARRY R.	HOPKINTON	MASSACHUSETTS

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**Inventor Name Search Result**

Your Search was:

Last Name = TATE

First Name = LARRY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09899663</u>	Not Issued	161	07/05/2001	Systems, methods and methods for doing business for information retrieval	TATE, LARRY
<u>11479888</u>	Not Issued	30	06/30/2006	Testing microelectronic devices using electro-optic modulator probes	TATE, LARRY
<u>11479889</u>	Not Issued	30	06/30/2006	Circuit board-to-circuit board connectors having electro-optic modulators	TATE, LARRY
<u>11647039</u>	Not Issued	25	12/28/2006	Providing error correction coding for probed data	TATE, LARRY
<u>60216577</u>	Not Issued	159	07/07/2000	Systems, methods and methods for doing business for information retrieval	TATE, LARRY
<u>11373176</u>	Not Issued	30	03/13/2006	Renewable fuel/lubricant mixture for use in a two-stroke internal combustion engine	TATE, LARRY CHRISTOPHER
<u>60660330</u>	Not Issued	159	03/11/2005	Renewable fuel/lubricant mixture for use in a two-stroke internal combustion engine	TATE, LARRY CHRISTOPHER
<u>07929710</u>	<u>5255663</u>	250	08/12/1992	STOVE HEAT TRANSFER UNIT WITH IMPROVED ATTACHING MECHANISM	TATE, LARRY M.
<u>08977912</u>	<u>6029267</u>	150	11/25/1997	SINGLE-CYCLE, SOFT DECISION, COMPARE-SELECT OPERATION USING DUAL-ADD PROCESSOR	TATE, LARRY R
<u>09930275</u>	Not Issued	168	08/16/2001	High speed, low latency bus modulation method and apparatus including a semiconductor structure for implementing same	TATE, LARRY R.
<u>10629325</u>	Not	61	07/28/2003	Signaling with multiple clocks	TATE, LARRY R.

	Issued				
<u>10813143</u>	Not Issued	30	03/30/2004	Method and apparatus to perform modulation using integer timing relationships between intra symbol modulation components	TATE, LARRY R.
<u>11322510</u>	Not Issued	30	12/30/2005	Transmit equalizer compensation for probe receivers	TATE, LARRY R.
<u>07178982</u>	<u>4967388</u>	150	04/21/1988	TRUNCATED PRODUCT PARTIAL CANONICAL SIGNED DIGIT MULTIPLIER	TATE, LARRY R.
<u>08925302</u>	<u>6446193</u>	150	09/08/1997	METHOD AND APPARATUS FOR SINGLE CYCLE PROCESSING OF ACCUMULATOR PAIRS IN A DUAL MULTIPLY-ACCUMULATE ARCHITECTURE	TATE, LARRY R.
<u>08925362</u>	<u>6009128</u>	150	09/08/1997	METRIC ACCELERATION ON DUAL MAC PROCESSOR	TATE, LARRY R.
<u>08925517</u>	Not Issued	161	09/08/1997	VITERBI COMPARE SELECT OPERATION FOR TWO-BIT TRACEBACK CODING	TATE, LARRY R.
<u>08925607</u>	<u>6467035</u>	150	09/08/1997	SYSTEM AND METHOD FOR PERFORMING TABLE LOOK-UPS USING A MULTIPLE DATA FETCH ARCHITECTURE	TATE, LARRY R.
<u>08925608</u>	Not Issued	161	09/08/1997	MODE-CONTROLLED FEEDBACK ARCHITECTURE AND METHOD	TATE, LARRY R.
<u>08927558</u>	<u>5889689</u>	150	09/08/1997	HIERARCHICAL CARRY-SELECT, THREE-INPUT SATURATION	TATE, LARRY R.
<u>08932977</u>	<u>6073228</u>	150	09/18/1997	MODULO ADDRESS GENERATOR FOR GENERATING AN UPDATED ADDRESS	TATE, LARRY R.
<u>08978468</u>	Not Issued	161	11/25/1997	MULTIPLE DATAPATH PROCESSOR WITH A THREE-INPUT ADDER	TATE, LARRY R.
<u>09074669</u>	<u>6272188</u>	150	05/08/1998	SINGLE-CYCLE ACCELERATOR FOR	TATE, LARRY R.

				EXTREMUM STATE SEARCH	
<a href="#">09131440</a>	<a href="#">6115805</a>	150	08/07/1998	NON-ALIGNED DOUBLE WORD FETCH BUFFER	TATE, LARRY R.
<a href="#">09132882</a>	<a href="#">6530014</a>	150	08/12/1998	NEAR-ORTHOGONAL DUAL-MAC INSTRUCTION SET ARCHITECTURE WITH MINIMAL ENCODING BITS	TATE, LARRY R.
<a href="#">60058127</a>	Not Issued	159	09/08/1997	NON-ALIGNED DOUBLE WORD FETCH BUFFER	TATE, LARRY R.
<a href="#">60066693</a>	Not Issued	159	11/24/1997	SINGLE-CYCLE ACCELERATOR FOR EXTREMUM STATE SEARCH	TATE, LARRY R.
<a href="#">60279485</a>	Not Issued	159	03/29/2001	Online virtula party concept	TATE, LARRY WAYNE

Inventor Search Completed: No Records to Display.

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